

driving chip and the pixel electrodes. The material for forming the transparent conductive lines includes indium tin oxide and indium zinc oxide. In addition, the cap is located above the display area of the substrate to encapsulate the pixel structures.

[0015] The present invention uses transparent conductive lines to provide electric connection between the pixel structures and the driving chip instead of using the fan-outs described above. Therefore, the problem of incomplete curing the UV glue is resolved in the present invention.

[0016] As the display area is sealed within the cap and the completely cured photosensitive glue, the internal device of the display will not be damaged by the moisture penetration.

[0017] The present invention thus improves the defect of the conventional structure and process, such that the reliability of the display is enhanced.

BRIEF DESCRIPTION OF DRAWINGS

[0018] These, as well as other features of the present invention, will become more apparent upon reference to the drawings.

[0019] Figure 1 shows a top view of a conventional active matrix organic light emitting diode display.

- [0020] Figure 2 shows a cross sectional view along the line I-I" as shown in Figure 1.
- [0021] Figure 3 shows a top view of an active matrix organic light emitting diode display according to the present invention.
- [0022] Figure 4 shows a cross sectional view along the line II-II" as shown in Figure 3.

DETAILED DESCRIPTION

- [0023] Figure 3 shows a top view of an active matrix organic light emitting diode provided by the present invention, and Figure 4 shows a cross sectional view along line II-II" as shown in Figure 3.
- [0024] Referring to Figures 3 and 4, a substrate 100 is provided. The substrate 100 comprises a display area 120 and a non-display area 122. A plurality of pixel structures 107 is formed in the display area 120. Each of the pixel structures 107 comprises an active device (thin-film transistor), an anode layer 102, a luminescent layer 104 and a cathode layer 106.
- [0025] While forming the anode layer 102 of the pixel structures 107, a plurality of transparent conductive lines 300 are simultaneously defined in the non-display area 120 extending from two edges of the display area 120 to the non-display area 122 for forming a plurality of fan-outs.